



Course Outline

Instructors

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Office Hours

- After lectures or by appointment

Teaching Assistant

- Dyar Aminyan
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- Mewan Peiris
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Contact Hours

- 5 credit hours
- Lectures:
3 hours x 12 weeks = 36 hours
- Laboratories:
3 hours x 10 weeks = 30 hours
- Tutorials: (optional)
3 hours x 12 weeks = 36 hours

Prerequisites:

- ENG 2262 Electric Circuits

Traditional Territories Acknowledgement

The University of Manitoba campuses and the Department of Electrical and Computer Engineering are located on original lands of the Anishinaabeg, Cree, Ojibwe-Cree, Dakota, and Dene peoples, and on the homeland of the Red River Métis.

We respect the Treaties that were made on these territories, we acknowledge the harms and mistakes of the past, and we dedicate ourselves to move forward in partnership with Indigenous communities in a spirit of Reconciliation and collaboration.

ECE 2160 – Electronics 2E

Winter 2024

Course Objectives

To understand the principles, circuit models and applications of amplifiers, real and ideal op amps, diodes and transistors, and to design simple circuits using these components.

Course Content

The following topics will be covered:

- Review of Circuit Theory
- Signals and Amplifiers
- Circuit Models of Amplifiers
- Frequency Response of Amplifier Blocks
- Ideal Op Amps and Applications
- Non-ideal behaviour in Op Amps
- Diode: Real and Ideal behaviours
- Diode: Circuits and Applications
- Bipolar Junction Transistors (BJT)
- MOS Field Effect Transistors (MOSFETs)
- BJT and FET Equivalent circuit Models and Amplifiers

CAD Tools

- National Instruments™ Multisim™
- Texas Instruments TINA-TI V9 (optional). <http://www.ti.com/tool/tina-ti>

Textbook

Microelectronic Circuits, A.S. Sedra, K.C. Smith, T. C. Carusone, and V. Gaudet, 8th Edition, Oxford, 2020.

Learning Outcomes

1. Ideal Op Amps (inverting/non-inverting configurations, summers, voltage/current amplifiers, precision rectifiers, integrators/differentiators).
2. Non-ideal Op Amps (input voltage offset, input bias currents, slew rate, output voltage/current saturation).
3. Diodes (physics of pn-junction, models, rectifiers, voltage regulators).
4. Transistors (physical operation, models, I-V terminal characteristics, biasing schemes, common-emitter, common-base, collector amplifiers, two stage amplifiers).

Expected Competency Levels

Outcome	KB	PA	IN	DE	ET	IT	CS	PR	IE	EE	EP	LL
1	D	D	D	D	D							
2	D	I	I	I	D							
3	D	D	D	I	D							
4	D	D	D	I	D							

Evaluation

Students must receive a minimum of 50% on the final examination in order to be eligible to receive a passing grade. Students who are unable to write term tests for medical (or other acceptable) reasons will have their final examination weighted to include the term test weighting. Students must complete all the laboratories in order to be eligible to receive a passing grade.

Important Dates

- **Term Test**
Thursday, February 29th, 2024
6:00PM–8:00PM
- **Voluntary Withdrawal Deadline**
March 20th, 2024
- **Louis Riel Day**
February 19th, 2024
No classes or examinations
- **Spring Break**
February 20th–23rd, 2024
No classes or examinations
- **Good Friday**
March 29th, 2024
No classes or examinations

Accreditation Details

Accreditation Units

- Mathematics: 0%
- Natural Science: 0%
- Complementary Studies: 0%
- Engineering Science: 75%
- Engineering Design: 25%

Graduate Attributes

KB: A knowledge base for engineering
PA: Problem analysis
IN: Investigation
DE: Design
ET: Use of engineering tools
IT: Individual and team work
CS: Communication skills
PR: Professionalism
IE: Impact of engineering on society/
environment
EE: Ethics and equity
EP: Economics and project
management
LL: Life-long learning

Competency Levels

I - Introduced (Introductory)
D - Developed (Intermediate)
A - Applied (Advanced)

Component	Value (%)	Method of Feedback	Learning Outcomes Evaluated
Assignments	0	F	1, 2, 3, 4
Quizzes	10	F, S	1, 2, 3, 4
Laboratories	20	S	1, 2, 3, 4
Term Test	20	S	1, 2, 3
Final Examination	50	S	1, 2, 3, 4

* Method of Feedback: F - Formative (written comments and/or oral discussion), S - summative (numerical grade)

CEAB Graduate Attributes Assessed

PA.3 – Analyzes and solves complex engineering problems.

DE.1 – Understands the complexities of an open-ended engineering design problem and defines appropriate objectives and constraints.

Student Absences

Attendance in lectures, tutorials, and laboratories is mandatory. For short-term absences due to illness or other extenuating circumstances of 120 hours (5 days) or less, students are required to complete a *Self-Declaration Form for Brief or Temporary Absence* available on the University website. This form must be submitted to the course instructor within 48 hours of the absence. (No additional documentation is required.)

Note that students are responsible to complete any missed work and must consult with the instructor to make appropriate arrangements.

For absences longer than 120 hours, students must contact the instructor and ECE Undergraduate Advisor, Tammy Holowachuk (Tammy.Holowachuk@umanitoba.ca) for further instructions.

Deferred Final Examinations

Students who miss the regular scheduled writing of a final examination, for valid medical or compassionate reasons, may be given the opportunity to write a deferred examination, subject to approval by the Associate Dean (Undergraduate). All requests for a deferred examination must be made within 48 hours of the missed examination, and must follow the procedure described on the Faculty website, without exception. Course instructors do not have the discretion to grant deferred final examinations.

(<https://umanitoba.ca/engineering/student-experience#engineering-student-policies>)

Requirements/Regulations

- Attendance at lectures and laboratories is essential for successful completion of this course. Students must satisfy each evaluation component in the course to receive a passing final grade.
- It is the responsibility of each student to contact the instructor in a timely manner if he or she is uncertain about his or her standing in the course and about his or her potential for receiving a failing grade. Students should also familiarize themselves with the University's *General Academic Regulations*, as well as Section 3 of the Faculty of Engineering *Academic Regulations* dealing with incomplete term work, deferred examinations, attendance and withdrawal.
- No programmable devices or systems (such as calculators, PDAs, iPods, iPads, cell phones, smart watches, wireless communication or data storage devices) are allowed in examinations unless approved by the course instructor.
- Students should be aware that they have access to an extensive range of resources and support organizations. These include Academic Resources, Counselling, Advocacy and Accessibility Offices as well as documentation of key University policies e.g. Academic Integrity, Respectful Behaviour, Examinations and related matters.

 [Supplemental Resources](#)

Grading Scale

Letter	Mark
A+	95–100
A	85–94
B+	80–84
B	70–79
C+	65–69
C	55–64
D	45–54
F	< 45

Note: These boundaries represent a guide for the instructor and class alike. Provided that no individual student is disadvantaged, the instructor may vary any of these boundaries to ensure consistency of grading from year-to-year.

Academic Integrity

Students are expected to conduct themselves in accordance with the highest ethical standards of the Profession of Engineering and evince academic integrity in all their pursuits and activities at the university. As such, in accordance with the *General Academic Regulations on Academic Integrity*, students are reminded that plagiarism or any other form of cheating in examinations, term tests, assignments, projects, or laboratory reports is subject to serious academic penalty (e.g. suspension or expulsion from the faculty or university). A student found guilty of contributing to cheating by another student is also subject to serious academic penalty.

Copyright Notice

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Retention of Student Work

Students are advised that copies of their work submitted in completing course requirements (i.e. assignments, laboratory reports, project reports, test papers, examination papers, etc.) may be retained by the instructor and/or the department for the purpose of student assessment and grading, and to support the ongoing accreditation of each Engineering program. This material shall be handled in accordance with the University's *Intellectual Property Policy* and the protection of privacy provisions of *The Freedom of Information and Protection of Privacy Act (Manitoba)*. Students who do not wish to have their work retained must inform the Head of Department, in writing, at their earliest opportunity.