ECE 3740 – Systems Engineering Principles 1

Course Objectives

This course teaches systematic approaches to the design and development of large and complex computer based systems. The systematic approaches include hardware and software architecture and architectural elements; design patterns; object oriented design, analysis, and synthesis; and hardware and software engineering. This course uses the project based learning and hands-on experiential learning method to develop design thinking, critical problem solving, communication, and teamwork skills. Students will work throughout the course individually and in coordinated teams on designing hardware and software for solving an authentic real world computer engineering problem. Students are evaluated by their demonstration of the specified CEAB attributes and performance in the hands-on sub-projects, midterm test, and final exam.

Course Content

The following topics will be covered:

- Principles of object orientation: class/object, information hiding, polymorphism, inheritance/interface
- Principles of object oriented analysis: modeling, domain analysis, requirements engineering, problem breakdown and analysis
- Principles of object oriented synthesis: divide and conquer, minimizing complexity, maximizing cohesion, architectural patterns, design patterns, designing for reuse, and reusing designs
- Java and C programming languages for software descriptions
- Modeling: Unified Modeling Language (UML) and XML
- Debugging, verification, and validation
- Use of TCP/IP Stack software and associated tools
- Test Plan and Procedures (unit and system tests) and design documentation.

Projects

- Client-Server architecture and socket design
- Developing TCP/UDP client-servers across different platforms
- Console and Graphical User Interface (GUI) design
- Interfacing with sensors for environmental monitoring and control using PMODs
- Service oriented architecture application, design and integration

Textbook

Course notes available online.

Other Resources


Academic Integrity

Students are expected to conduct themselves in accordance with the highest ethical standards of the Profession of Engineering and evince academic integrity in all their pursuits and activities at the university. As such, in accordance with the General Academic Regulations on Academic Integrity, students are reminded that plagiarism or any other form of cheating in examinations, term tests, assignments, projects, or laboratory reports is subject to serious academic penalty (e.g. suspension or expulsion from the faculty or university). A student found guilty of contributing to cheating by another student is also subject to serious academic penalty.

Retention of Student Work

Students are advised that copies of their work submitted in completing course requirements (i.e. assignments, laboratory reports, project reports, test papers, examination papers, etc.) may be retained by the instructor and/or the department for the purpose of student assessment and grading, and to support the ongoing accreditation of each Engineering program. This material shall be handled in accordance with the University’s Intellectual Property Policy and the protection of privacy provisions of The Freedom of Information and Protection of Privacy Act (Manitoba). Students who do not wish to have their work retained must inform the Head of Department, in writing, at their earliest opportunity.
Learning Outcomes

1. Identify the importance of applying systems engineering principles to design of large and complex systems.
2. Perform requirements engineering for large and complex software design.
3. Model the designs of large and complex systems using UML.
4. Apply the main object oriented principles to the design of large and complex embedded systems.
5. Demonstrate the ability to use an integrated development environment to develop embedded applications on host computers, servers, and microcontrollers.
6. Construct formal test cases and test plans.
7. Write design documentation.

Expected Competency Levels

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Evaluation

The final course grade will be determined from a student’s performance in the projects and on examinations. In order to receive a passing grade in this course:

- All projects must be completed and a passing grade must be achieved.
- A passing grade in the final exam must be achieved.

Requirements and Regulations

- Attendance at lectures and laboratories is essential for successful completion of this course. Students must satisfy each evaluation component in the course to receive a final grade.
- It is the responsibility of each student to contact the instructor in a timely manner if he or she is uncertain about his or her standing in the course and about his or her potential for receiving a failing grade. Students should also familiarize themselves with the University’s General Academic Regulations, as well as Section 3 of the Faculty of Engineering Academic Regulations dealing with incomplete term work, deferred examinations, attendance and withdrawal.
- No programmable devices or systems (such as calculators, PDAs, iPods, iPads, cell phones, wireless communication or data storage devices) are allowed in examinations unless approved by the course instructor.
- Students should be aware that they have access to an extensive range of resources and support organizations. These include Academic Resources, Counselling, Advocacy and Accessibility Offices as well as documentation of key University policies e.g. Academic Integrity, Respectful Behaviour, Examinations and related matters.